



IFU/ 2634.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Applicant: | § | |
| Daniel Yellin et al. | § | Art Unit: 2634 |
| | § | |
| Serial No.: 10/039,072 | § | Examiner: Cicely Q. Ware |
| | § | |
| Filed: January 2, 2002 | § | Docket: ITL.0613US |
| | § | P11754 |
| For: Robust Low Complexity Multi-Antenna Adaptive Minimum Mean Square Error Equalizer | § | Assignee: Intel Corporation |
| | § | |

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 3

Sir:

In response to the office action mailed March 16, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: May 17, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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